In The United States Patent and Trademark Office

Serial Number:

10/718383

Appn. Filed:

11-19-03

Applicant:

T. Massingill

Appn. Tj

Semiconductor Package with Recess for Die

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Feb. 4, 2004

At:

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Information Disclosure Statement

Commissioner for Patents PO Box 1450. Alexandria, VA 22313-1450

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Respectfully,

Applicant:

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Compl te if Known			
Application Number	10/718383		
Filing Date	11-19-03		
First Named Inventor	Thomas Massinsill		
Art Unit			
Examiner Name			
Attorney Docket Number			

			U. S. PATENT D	OCUMENTS		
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (f known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		us- 5420460	05-30-1995	Massingill	pg2 fig 2	
		US- 5869 889	02-09-1999	Massingill Chia	0913 fig 4	
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